

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of

Atty Dkt. 900-400

C# M#

AOKI, Hitoshi

OCT 04 2002

Group Art Unit: 2822

Serial No. 09/960,517

Examiner: Thomas, Toniae M.

Filed: September 24, 2001

Date: October 4, 2002

Title: SEMICONDUCTOR DEVICE AND FABRICATION PROCESS THEREFOR

Assistant Commissioner for Patents
Washington, DC 20231

Sir:

RESPONSE/AMENDMENT/LETTER

This is a response/amendment/letter in the above-identified application and includes an attachment which is hereby incorporated by reference and the signature below serves as the signature to the attachment in the absence of any other signature thereon.

Fees are attached as calculated below:

Total effective claims after amendment 16 minus highest number
previously paid for 20 (at least 20) = 0 x \$ 18.00 \$ 0.00

Independent claims after amendment 4 minus highest number
previously paid for 3 (at least 3) = 1 x \$ 84.00 \$ 84.00

If proper multiple dependent claims now added for first time, add \$280.00 (ignore improper) \$ 0.00

Petition is hereby made to extend the current due date so as to cover the filing date of this
paper and attachment(s) (\$110.00/1 month; \$400.00/2 months; \$920.00/3 months) \$ 0.00

Terminal disclaimer enclosed, add \$ 110.00 \$ 0.00

☐ First/second submission after Final Rejection pursuant to 37 CFR 1.129(a) (\$740.00) \$ 0.00
☐ Please enter the previously unentered, filed
☐ Submission attached

Subtotal \$ 84.00

If "small entity," then enter half (1/2) of subtotal and subtract -\$ 0.00
☐ Applicant claims "small entity" status. ☐ Statement filed herewith

Rule 56 Information Disclosure Statement Filing Fee (\$180.00) \$ 0.00

Assignment Recording Fee (\$40.00) \$ 0.00

Other: 0.00

TOTAL FEE ENCLOSED \$ 84.00

The Commissioner is hereby authorized to charge any deficiency, or credit any overpayment, in the fee(s) filed, or asserted to be filed, or which should have been filed herewith (or with any paper hereafter filed in this application by this firm) to our Account No. 14-1140. A duplicate copy of this sheet is attached.

1100 North Glebe Road, 8th Floor
Arlington, Virginia 22201-4714
Telephone: (703) 816-4000
Facsimile: (703) 816-4100
JAR:caj

NIXON & VANDERHYE P.C.
By Atty: Joseph A. Rhoa, Reg. No. 37,515

Signature: 



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of

AOKI, Hitoshi

Atty. Ref.: 900-400

Serial No. 09/960,517

Group: 2822

Filed: September 24, 2001

Examiner: Thomas, Toninae

For: SEMICONDUCTOR DEVICE AND FABRICATION PROCESS THEREFOR

* * * * *

October 4, 2002

Assistant Commissioner for Patents
Washington, DC 20231

Sir:

AMENDMENT

Responsive to the Official Action dated July 5, 2002, please amend the
above-identified application as follows:

IN THE CLAIMS

Please cancel claims 13-17, without prejudice in view of the Restriction
Requirement.

Please substitute the following amended claim(s) for corresponding claim(s)
previously presented. A copy of the amended claim(s) showing current revisions is
attached.

1. (Amended) A semiconductor device comprising:
a semiconductor substrate of a first conductivity type;

10/07/2002 HDEMESS1 00000039 09960517

01 FC:102

84.00 OP

#671515 v1 - amendment